## Docket Number (Optional) Application Number POU920000088US1 Applicant(s) INFORMATION DISCLOSURE CITATION Chen et al. (Use several sheets if necessary) Filing Date Herewith U.S. PATENT DOCUMENTS EXAMINER FILING DATE SUBCLASS DATE NAME CLASS DEE DOCUMENT NUMBER INITIAL IF APPROPRIATE 06/14/94 380 24 5321752 Iwamura et al. 04/30/96 Cressel et al. 364 754 5513133 364 746 5764554 06/09/98 Monier FOREIGN PATENT DOCUMENTS Translation CLASS SUBCLASS DATE COUNTRY REF DOCUMENT NUMBER YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Kornerup, Peter, "A Systolic, Linear-Array Multiplier for a Class of Right-Shift Algorithms," IEEE Transacti ns on Computers, Vol. 43, No. 8, August, 1994, pp 892-898.

Montgomery, Peter, "Modular Multiplication Without Trial Division," Mathematics of Computation, Vol. 44, No.170, April 1985, pp. 519-521.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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